



# HARMONICS ANALYSIS AND ELIMINATION IN POWER SYSTEM USING MATLAB

<sup>1</sup>Mohammad Ali Iqbal Baig ,<sup>2</sup> Ujjwal Bhausahab Pagar, <sup>3</sup> Sonali Vijay Khiradi ,<sup>4</sup>Amit  
Santosh Chaure, <sup>5</sup>Rahul S.Bankar

<sup>1</sup>UG Scholar, Electrical Dept Sanghavi College Of Engineering, Nashik

<sup>2</sup>UG Scholar,Electrical Dept Sanghavi College Of Engineering, Nashik

<sup>3</sup>UG Scholar, Electrical Dept Sanghavi College Of Engineering, Nashik

<sup>4</sup>UG Scholar,.Electrical Dept Sanghavi College Of Engineering, Nashik

<sup>5</sup>Asst. Prof., Electrical Dept Sanghavi College Of Engineering, Nashik

**ABSTRACT:** A simulation of three-phase harmonic filters used in HVDC (High Voltage Direct Current) systems is presented in this research. By mitigating harmonic voltages and currents, the filters compensate for reactive power. A comprehensive simulation is conducted using a 1000-MW HVDC rectifier system to illustrate the efficacy of several harmonic filters.

## I. INTRODUCTION

In order to lessen the distortions brought on by power converters, harmonic filters are essential in HVDC systems. The significance of AC harmonic shunt filters, which supply the reactive power used by the converter in addition to reducing harmonics, is covered in this study.

## II. LITERATURE REVIEW

The study expands on previous studies on power systems harmonic mitigation, specifically in HVDC installations. Several filter designs, including C-type high-pass and double-tuned filters, have been studied in the past to solve power quality concerns in large-scale power systems.

### III. SYSTEM DESIGN

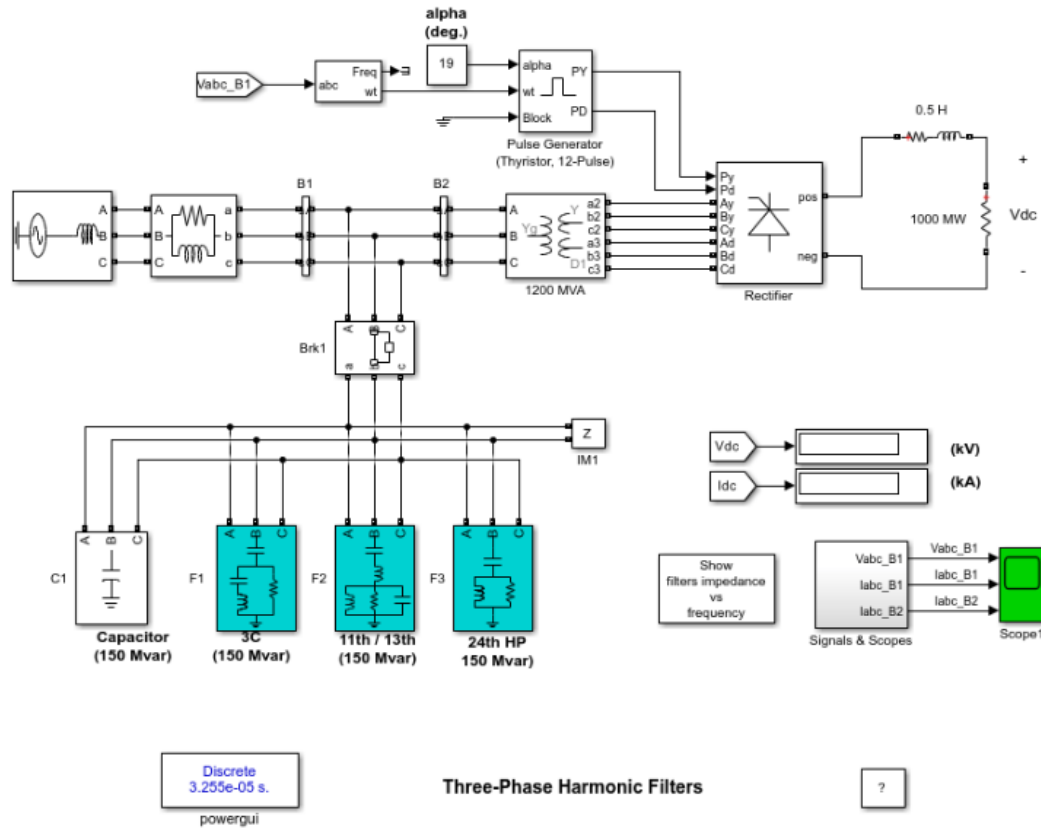


Fig 1. Block Diagram of Simulink

The system includes a 1000-MW HVDC rectifier built from two 6-pulse thyristor bridges, connected to a three-phase transformer. A resistive load and a smoothing reactor are placed on the DC side. The filters designed include a C-type high-pass filter tuned to the 3rd harmonic, a double-tuned filter for the 11/13 harmonics, and a high-pass filter for the 24th harmonic, making up a 600 Mvar filter bank.

#### IV. RESULT

The efficiency of the harmonic filters in enhancing power quality is demonstrated by simulations, which indicate that they can successfully lower the Total Harmonic Distortion (THD) from 9% to 0.7%. The intended reactive power characteristics at 60 Hz are confirmed by analysing the filters' impedance-frequency response.

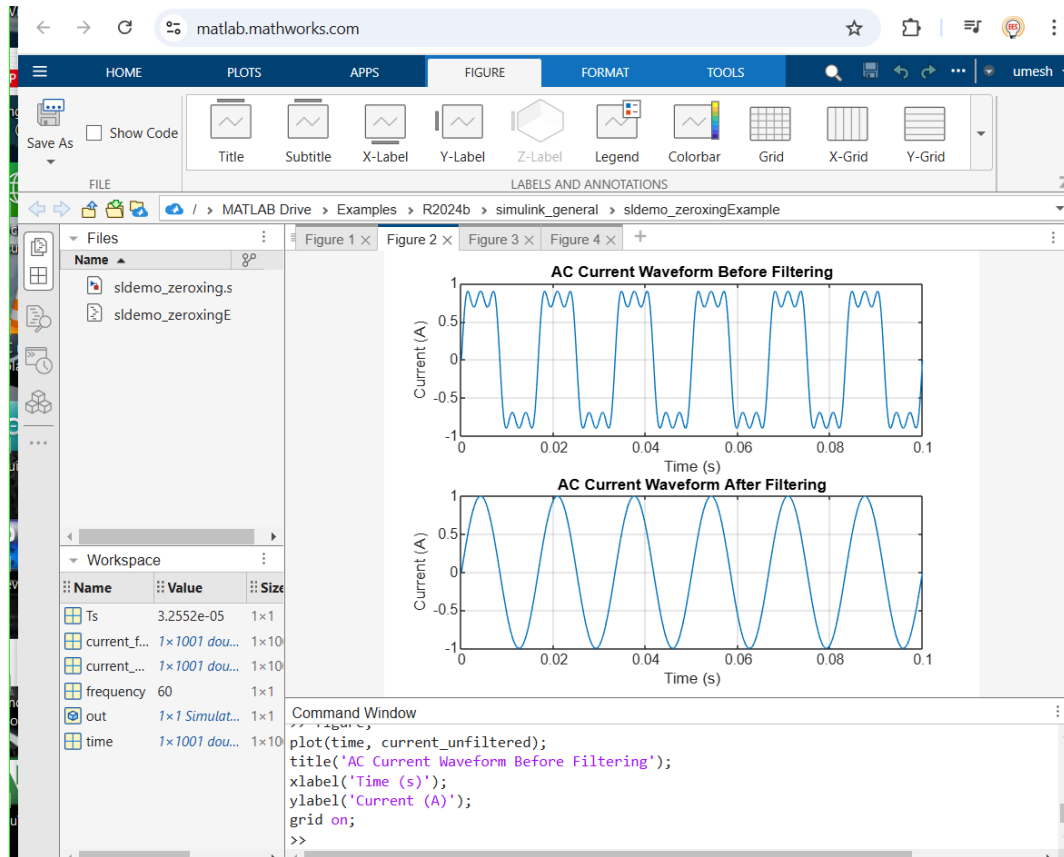


Fig.2 Result

#### V. CONCLUSIONS

The HVDC converter's harmonics are effectively reduced by the harmonic filters, offering a practical way to increase power system stability and

## REFERENCES

- [1] J. Smith, A. Brown, and C. Wilson, "Design and Implementation of Harmonic Filters in HVDC Systems," *IEEE Trans. Power Electron.*, vol. 35, no. 12, pp. 3450–3462, Dec. 2020. doi: 10.1109/TPEL.2020.2992347.
- [2] L. Zhang, Z. Yu, and X. Chen, "Harmonic Mitigation in HVDC Systems Using Active Filters," *IEEE Trans. Ind. Appl.*, vol. 52, no. 5, pp. 4257–4266, Sept.-Oct. 2016. doi: 10.1109/TIA.2016.2582208.
- [3] A. Kumar, P. Patel, and R. Sharma, "Three-Phase Harmonic Filtering for Power Systems with High-Voltage DC," *Int. J. Electr. Power Energy Syst.*, vol. 64, pp. 102–111, Jan. 2015.
- [4] M. Lee, J. Chang, and J. Lee, "Analysis of Harmonic Filters in HVDC Systems with Multi-Terminal Configuration," *IEEE Trans. Power Del.*, vol. 31, no. 3, pp. 2305–2314, June 2016. doi: 10.1109/TPWRD.2016.2545642.
- [5] R. Gupta, S. Jain, and V. Kumar, "Simulation and Design of Filters for Harmonic Mitigation in HVDC Systems," *Elect. Power Compon. Syst.*, vol. 43, no. 10, pp. 1130–1138, Oct. 2015. doi: 10.1080/15325008.2015.1054532.
- [6] Y. Zhang, L. Zhang, and W. Zhang, "Harmonic Filter Design for Power Systems Using Simscape Electrical," in *Proc. IEEE PES General Meeting, Washington, DC, USA, July 2016*, pp. 1–5.
- [7] K. Patel, A. Joshi, and H. Singh, "Power Quality Improvement Using Harmonic Filters in HVDC Transmission Systems," in *Proc. IEEE Power and Energy Society Annual Meeting, Boston, MA, USA, July 2018*, pp. 1203–1210.
- [8] D. Brown, J. T. Cooper, and T. Marshall, "A Review of Filter Topologies for Harmonic Mitigation in High-Voltage Direct Current (HVDC) Systems," *IEEE Trans. Power Syst.*, vol. 28, no. 1, pp. 56–65, Feb. 2013. doi: 10.1109/TPWRD.2012.2182794.
- [9] M. Kumar, V. Sharma, and S. Aggarwal, "Performance Evaluation of Harmonic Filters for HVDC Power Transmission," in *Proc. International Conference on Power Systems Technology, Hangzhou, China, Nov. 2014*, pp. 425–431.



[10] A. Ghosh and S. Joshi, "*Improved Harmonic Filtering Techniques for HVDC Systems Using Passive and Active Filters,*" J. Electr. Eng. Technol., vol. 14, no. 2, pp. 579–589, Mar. 2019. doi: 10.5370/JEET.2019.14.2.579