

A REVIEW ON PROTECTION OF SENSITIVE LOADS USING SLIDING MODE CONTROLLED THREE-PHASE DVR WITH ADAPTIVE NOTCH FILTER

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Abstract- This paper introduces a sliding mode control method for 3-phase dynamic voltage restorers with 12-switch voltage source inverter. The compensating voltage references in the SMC strategy are generated by using an adaptive notch filter which ensures high-quality overall performance under grid voltage anomalies which includes voltage sags, swells, unbalanced and distorted grid voltage conditions. The effect of the use of an adaptive notch filter eliminates the usage of phase lock loop or frequency lock loop and low bypass filter which makes it distinguishable from the present reference signal production. In addition, the usage of SMC approach with its appealing features makes the control implementation simple. Dynamic Voltage Restorer is to inject a voltage in series with the grid voltage to keep the load voltage at required level for all times. In order to accomplish this objective, the DVR should be controlled by an appropriate control strategy capable of offering a fast dynamic response, high robustness to parameter variations, sinusoidal load voltage with low total harmonic distortion (THD) and small steady-state errors.

Index Terms — Adaptive notch filter, dynamic voltage restorer,

I. INTRODUCTION

Nowadays electrical systems are extra touchy to power quality issues. Voltage sag/swell is certainly one of the great hassles that our energy device network is dealing with today. Without proper mitigation, such a hassle can motivate excessive hassle and may bring about failure of a device. Current developments in custom devices can remedy such a problem. DVR is one of the powerful answers for compensation of voltage sag/swell. This chapter offers a top-level view of DVR, its basic structure and working principle. The industrial and commercial clients of electrical electricity penetrated into the distribution system are increasingly disturbing a better strength excellent. The voltage distortions and fluctuations existing inside the distribution system may also adversely affect the touchy loads like computing systems, verbal exchange gadgets, manufacturing methods and adjustable speed drives. The voltage sag and swell troubles stand up via short-circuit faults inside the system. In the case of the voltage sags, operation of the sensitive hundreds employed in the enterprise are fantastically affected which ends up in tremendous fees due to the production loss. Within the literature, voltage sag and swell troubles are defined as a abrupt discount or rise of the voltages various from 10% to 90% throughout sag and one hundred ten% to a hundred and eighty% in the course of swell of its nominal price [1]. Beside these, because of the non-linear masses which includes motor drives or energy electronics devices, the burden currents may additionally contain modern harmonic components.

The proposed control method gives rapid dynamic reaction, online estimation of the voltage anomalies at the grid voltages, sturdy robustness to voltage sags and swells and simplicity in implementation. In 12-transfer DVR topology, as opposed to using a three-section transformer, 3 unmarred-segment transformers are used

with the purpose of lowering 0-collection additives. Furthermore, this mixture will increase the power and reliability of the machine.

II. PROPOSED METHODOLOGY

A DVR is a series connected custom device that injects the appropriate/desired voltage to the load bus in order to maintain the voltage profile. However, in standard condition it is in stand-by mode. The compensating voltage is injected by three single phase transformers whose property can be controlled. These voltages are in synchronism with the load voltage. DVR has three mode of operation:-

2.1 Protection mode:

In order to isolate DVR from the system during overload current caused by short circuit or large inrush current, bypass switches are provided. The current is supplied to the system using other path.

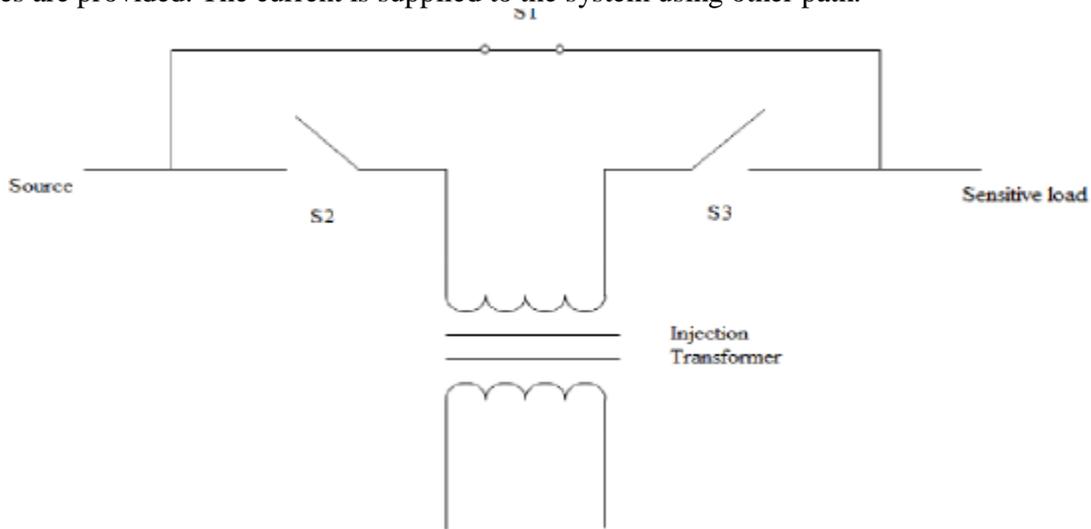


Fig.2.1 Protection mode

2.2 Standby mode:

In this mode, Low Voltage winding of injection transformer is shorted. No switching operation occurs,

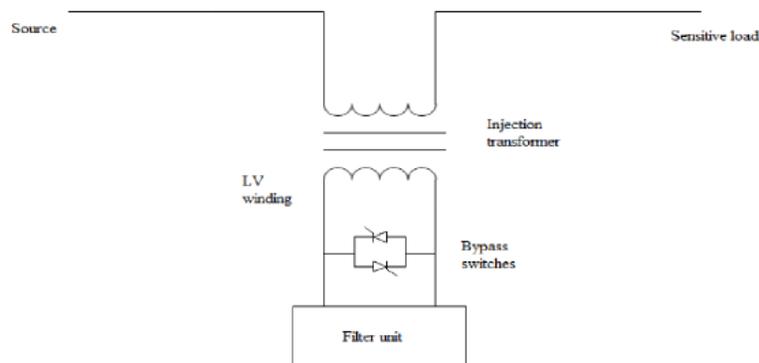


Fig.2.2 Standby mode

2.3 Injection mode:

The dynamic voltage restorer detects and compensates for sags within the voltage of the ac strength source so that the loads are insulated from these energy reliability troubles. The operating precept for the dvr . The dvr consists of dc electricity sources, an IGBT converter, and an injection transformer which is related in series with the electricity line and the sensitive load. The dc power assets that may be used are batteries, amazing capacitors, superconducting magnetic storage units, and flywheel. A sag inside the input ac energy line voltage can also propagate thru the energy community due to a fault in one of the distribution feeder strains. The dvr detects the sag and generates ac power from the dc electricity supply by means of using the igtb converter. The generated power is fed to the line with the aid of the transformer to accurate the sag in order that the sensitive load gets a surprisingly reliable ac input power.

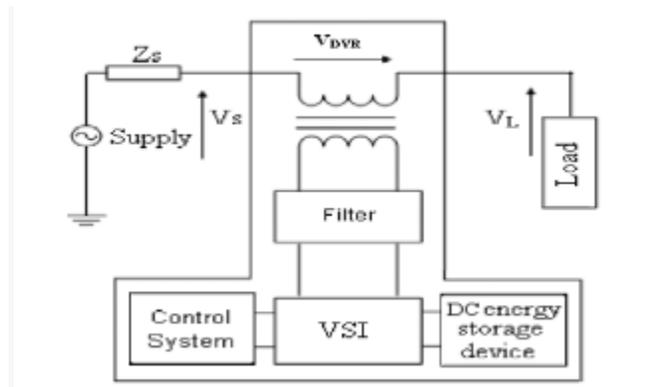


Fig 2.3 Dynamic voltage restorer (DVR) Configuration

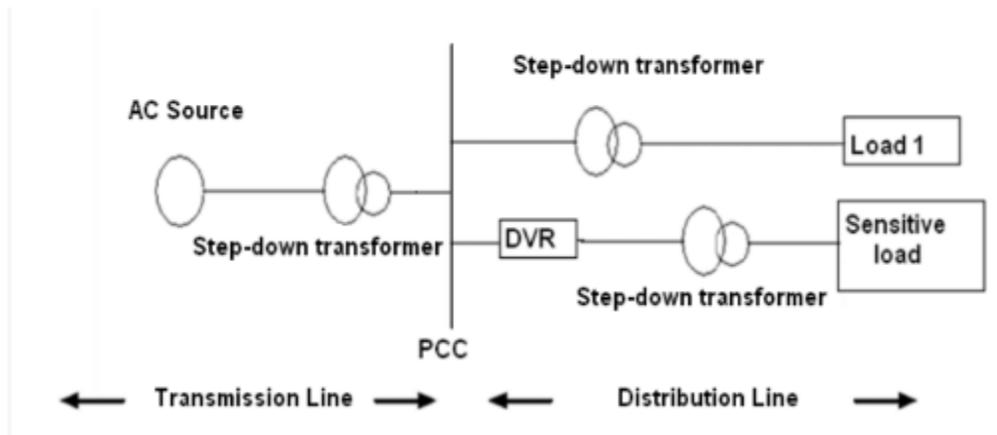


Fig 2.4 Location of a dynamic voltage restorer (DVR)

2.3.1 Principle of DVR

Dynamic voltage restorer (DVR) shields the load from voltage unsettling influences. DVR keeps up the load voltage at a foreordained level during any source voltage strange conditions, for example, voltage lists/swells or contortion. The working standard of the DVR can be clarified through the fig. Under ordinary working conditions, let the three stage voltage phasors V_{a1} , V_{b1} and V_{c1} . During anomalous conditions, the stage voltage vectors might be changed to V_{a2} , V_{b2} and V_{c2} . DVR doesn't flexibly any genuine force in the consistent state. This infers that the stage point distinction between DVR voltage phasor and current phasor

must be 90 degree in the consistent state. DVR infuses the required remunerating voltage through transformer. The transformer is associated in arrangement to the heap. DVR works just during the unusual conditions and stays inert during ordinary working conditions. During activity, DVR has a capacity to gracefully and retain dynamic and receptive force. Dynamic voltage restorer amends the load voltage by providing responsive force produced inside on the event of small fault.

2.3.2 DVR with Twelve switch VSI

The circuit of the considered DVR is a three-stage 12-switch VSI with a energy store battery as appeared in Fig. The vitality stockpiling is needed to give genuine capacity to the heap during voltage infusion to framework. The lead-corrosive batteries are acceptable capacity gadgets which can be utilized in DVR applications. One of the primary focal points of utilizing a typical battery source at the dc-interface is that the dc-connect voltage guideline with extra control calculation isn't needed.

The basic inverter geographies of three-stage DVR can be named: 6-switch three-stage inverter, 6-switch three-stage inverter with split-capacitor and 12-switch three-stage inverter.

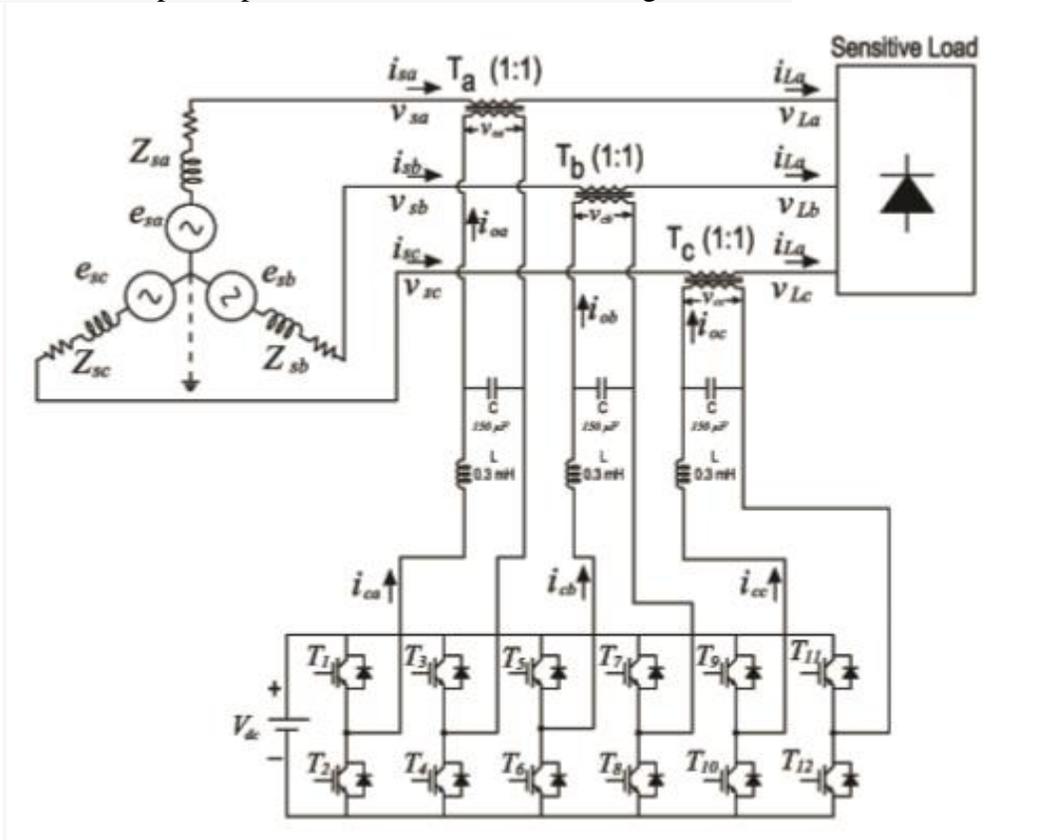


Fig.2.3.2 Power circuit of the three-phase twelve-switch DVR

III. SIMULATION & RESULT ANALYSIS

3.1 MATLAB Introduction

Matlab is a high-performance language for technical computing. The name mat lab stands for matrix laboratory. It integrates computation, visualization, and programming in an easy-to-use environment where problems and solutions are expressed in familiar mathematical notation. Typical uses include Math and computation Algorithm development Data acquisition Modelling, simulation, and prototyping Data analysis, exploration, and visualization Scientific and engineering graphics Application development, including graphical user interface building.

Matlab is an interactive system whose basic data element is an array that does not require dimensioning. This allows you to solve many technical computing problems, especially those with matrix and vector formulations, in a fraction of the time it would take to write a program in a scalar no interactive language such as C or FORTRAN.

3.2 History of Matlab:

Cleve Barry Moler, the chairman of the computer-science department at the University of New Mexico, he is a mathematician and computer programmer specializing in numerical analysis. Started developing MATLAB in the late 1970s. He designed it to give his students access to LINPACK and EISPACK without their having to learn Fortran. It soon spread to other universities and found a strong audience within the applied mathematics community. Jack Little, an engineer, was exposed to it during a visit Moler made to Stanford University in 1983. Recognizing its commercial potential, he joined with Moler and Steve Bangert. They rewrote MATLAB in C and founded Math Works in 1984 to continue its development. These rewritten libraries were known as JACKPAC. In 2000, MATLAB was rewritten to use a newer set of libraries for matrix manipulation, LAPACK.

3.3 System Simulation

The performance of the proposed control system has been tried by reproductions utilizing MATLAB/Simulink. The square graph of the shut circle framework is appeared in Fig. The framework and control boundaries utilized and trial contemplates are given in Reference section. Execution of the proposed control technique with the control strategies in [18] and [19] are thought about under four diverse network voltage conditions (case 1, case 2, case 3, and case 4). In the event that 1, it is accepted that a reasonable voltage hang happens (from 230V to 150V) in all periods of the network voltage for 50ms. On the off chance that 2, it is expected that a lopsided voltage list happens in stages An and B of the framework for 50ms. On the off chance that 3, the voltage swell of 120% (from 230V to 276V) happens in stages An and B of the matrix for 50ms. At long last, in the event that 4, the lattice voltage (all stages) is thought to be profoundly contorted for 50ms. The reproduction results with respect to these conditions are appeared in beneath Fig.

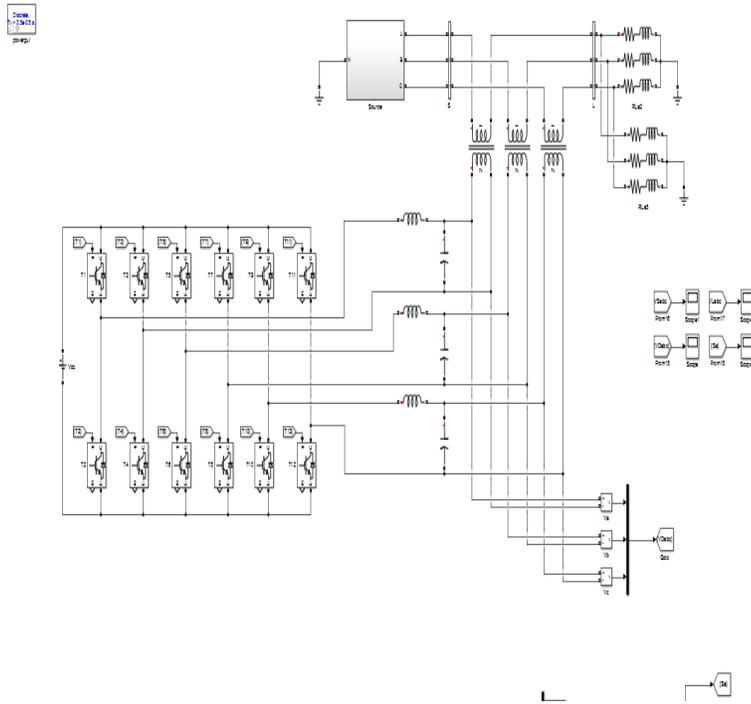
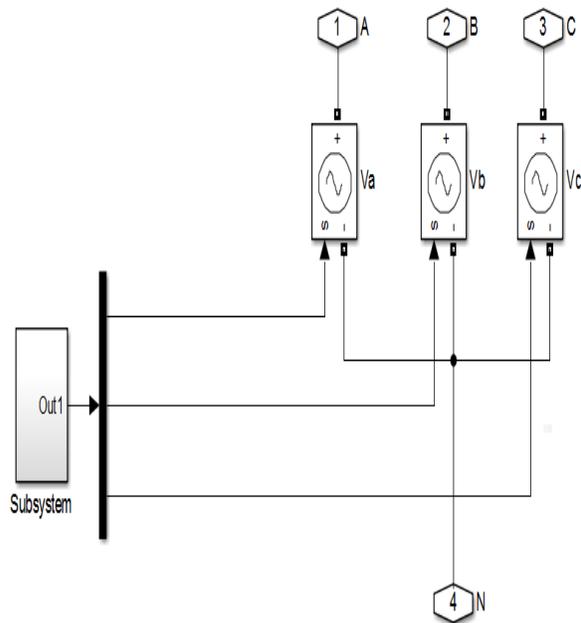


Fig.3.3 Simulink model of three-phase DVR with 12-switch VSI



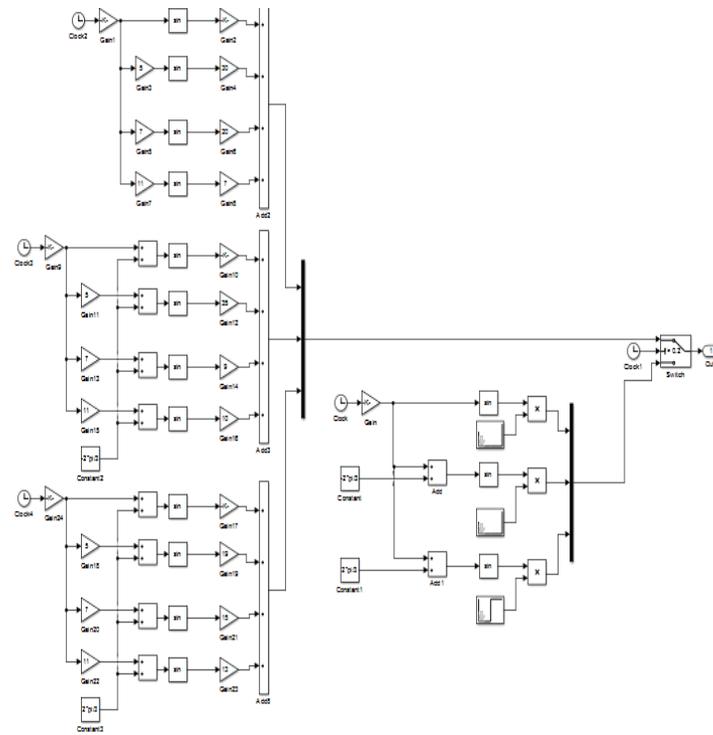


Fig. 3.3.1 Simulink model of Programmable three- phase supply

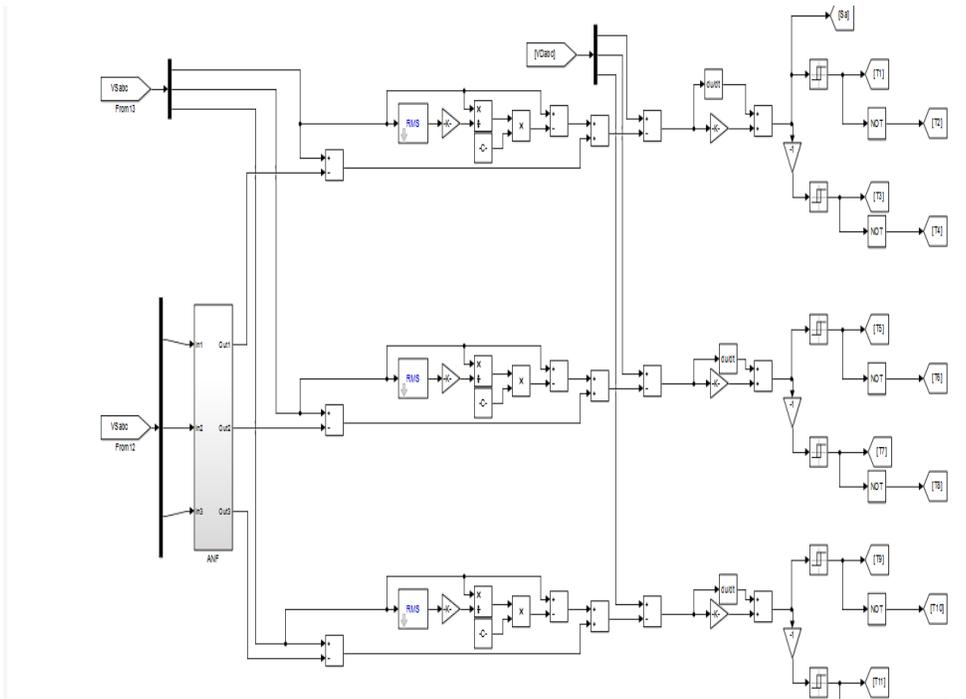


Fig. 3.3.2 Simulink model of Block diagram of the proposed control strategy.

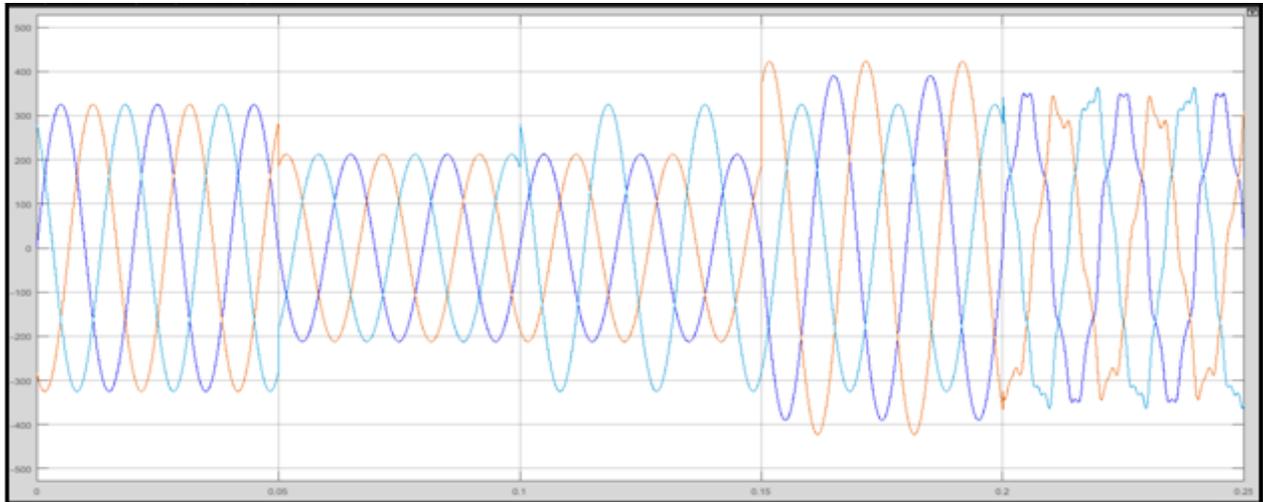


Fig. 3.3.3 Simulation results of Sending end three- phase Supply voltages

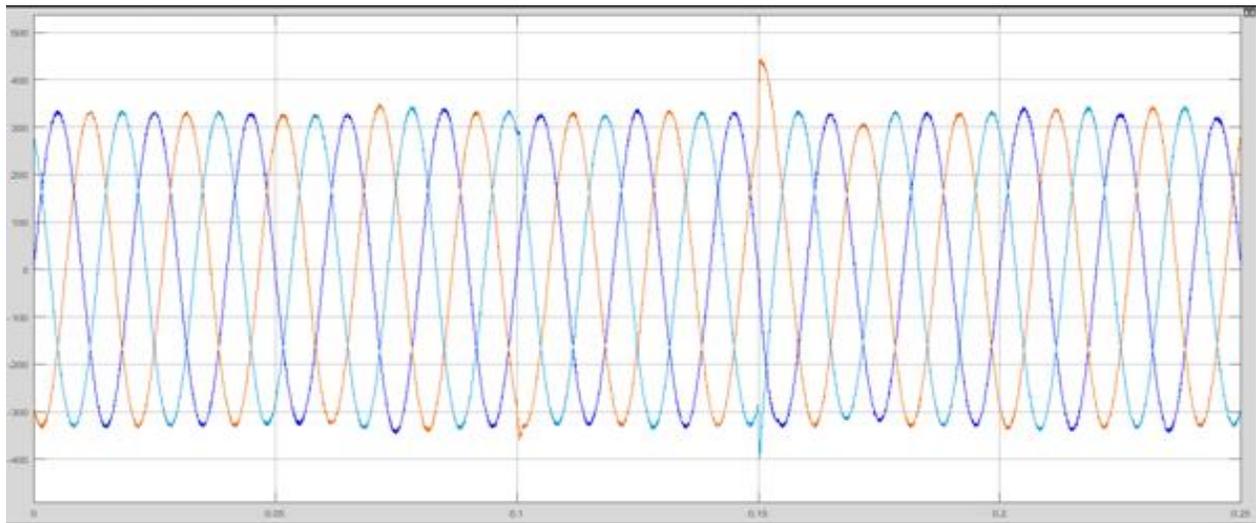


Fig. 4.14 Simulation Results of receiving end three- phase Supply voltages

3.4 Comparisons of the Proposed Method with the State of-The-Art-

It is well known that the occurrence and intensity of voltage-related power quality problems are increasing in grid. This is due to the increase in the number of voltage harmonic-generating devices. In order to overcome these voltage-related power quality problems, several DVR topologies and control methods have been investigated in the literature. A comparison of the proposed method with the state-of-the-art systems in terms of the various features of each method is provided in Table I. The first drawback of the methods presented in [6], [28] and [29] is that they need phase lock loop (PLL) or frequency lock loop (FLL) or low pass filter (LPF) in generating the compensating voltage references which increase the complexity. The proposed controller does not have such problem as ANF generates the compensating voltage references without using PLL, FLL, and LPF. The second drawback of these methods is that they compensate for voltage sags and swells only. In these studies, no result is provided to demonstrate the performance of the controller under distorted grid condition. In

contrast, the proposed method not only compensates voltage sags/swells, but also suppresses voltage harmonics on the load terminals and mitigates grid disturbances.

IV. Conclusion

A Sliding Mode Control methodology is proposed for three-phase DVR utilizing 12-switch VSI. Not quite the same as the current techniques, the compensating voltage references required in the SMC are produced by an ANF which shows astounding execution under framework voltage oddities, for example, voltage lists, expands, and lopsided and misshaped voltage conditions. Not at all like existing reference signal age arrangements, the proposed Versatile Step Channel doesn't need PLL or FLL and low pass channel. Besides, the utilization of SMC with its appealing properties makes the control execution straightforward. Hypothetical contemplations are confirmed by the reenactment results just as the ongoing research facility results over a scope of lattice voltage peculiarities. These outcomes show that the proposed control methodology not just offers an amazing unique reaction autonomous from the boundary varieties and aggravations, yet in addition remunerates the voltage lists, swells and music on the load terminals.

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